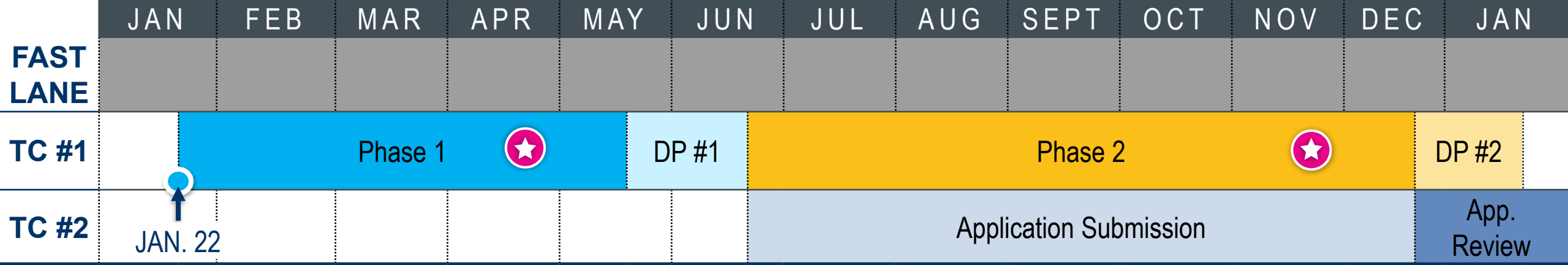




# Fast Lane Progress Update

March 2024 IPS

Dylan Seeley, Lead Engineer  
Interconnection Planning Projects



Fast Lane	Phase 1	Phase 2	Application Review Phase
<ul style="list-style-type: none"> <li>• Refreshed retool analysis (including short circuit and stability)</li> <li>• Facilities Studies (interconnection facilities &amp; network upgrades)</li> <li>• Final agreements</li> </ul>	<ul style="list-style-type: none"> <li>• Load flow analysis to determine Cycle upgrades</li> <li>• Planning-level costs to interconnect</li> </ul>	<ul style="list-style-type: none"> <li>• First retool</li> <li>• Short circuit</li> <li>• Stability</li> <li>• Interconnection Facilities Study</li> </ul>	<ul style="list-style-type: none"> <li>• Review applications</li> <li>• Build models</li> </ul>

☆ Notification of Decision Point start date



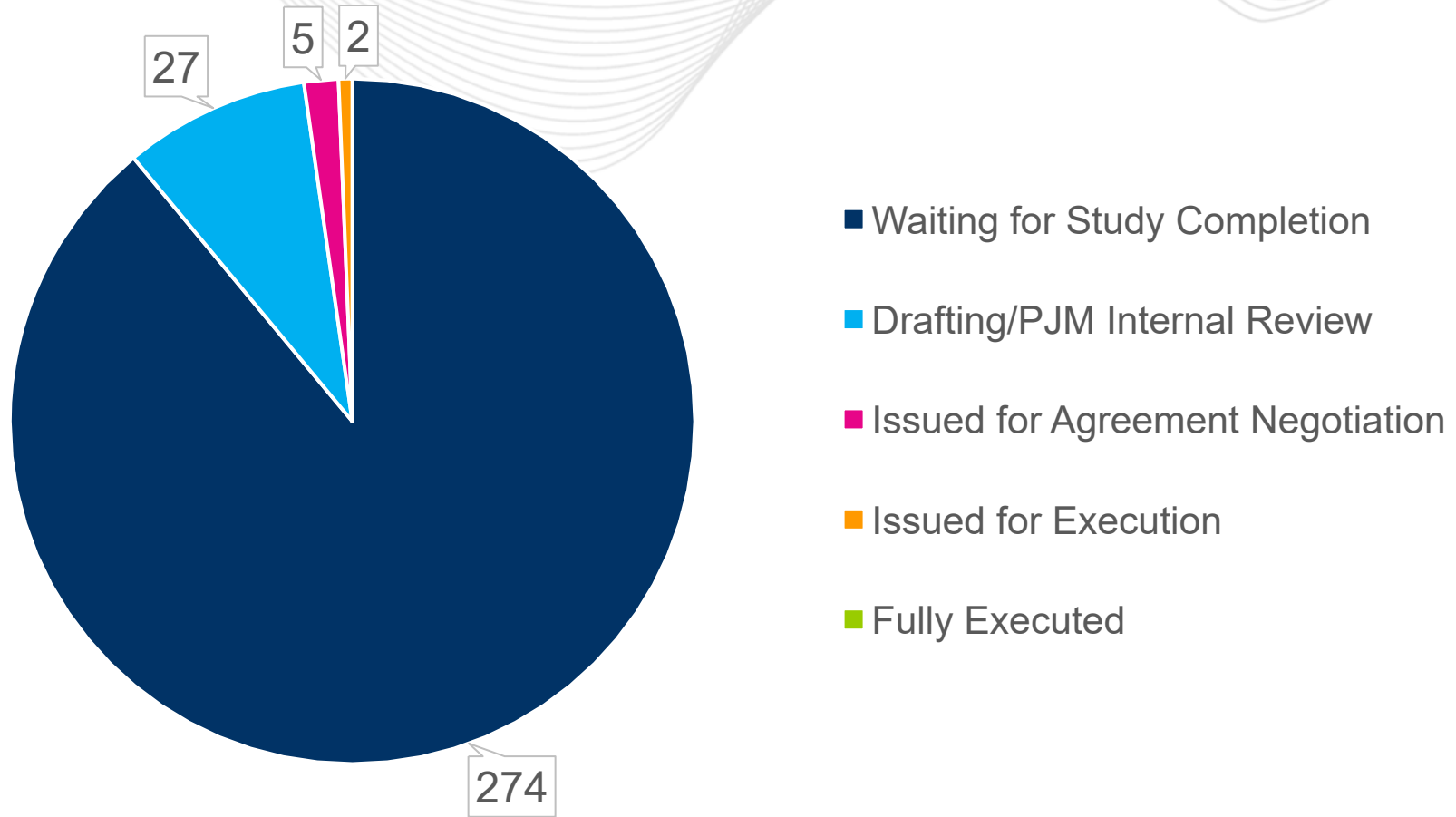
# Expedited Process Analysis Status

	AE1	AE2	AF1	AF2	AG1
Load Flow Model Status	Posted	Posted	Posted	Posted	Posted
Load Flow % Complete	100%	100%	100%	100%	97%
Short Circuit % Complete	100%	100%	100%	100%	83%
Stability % Complete	100%	83%	89%	74%	19%

*NOTE: Analyses considered Not Required are included in the completion percentages above*

- AE1-AG1 LF models (Summer Peak & Light Load) have been posted to [PJM.com](https://www.pjm.com)
- Analysis is in progress for AE1-AG1 projects in sequential queue order
- Expedited Process projects will receive SIS report after all analysis is completed, followed by Facilities Study Report (if required) and final GIA/WMPA
- For project-specific questions, please reach out to your PJM Project Manager

# Expedited Process Agreement Status



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